

Notice of References Cited	Application/Control No. 10/711,168	Applicant(s)/Patent Under Reexamination BREILAND ET AL.	
	Examiner Russell Frejd	Art Unit 2128	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Van Heijningen et al., High-Level Simulation of Substrate Noise Generation Including Power Supply Noise Coupling, Proceedings of the 37 th Conference on Design Automation, June 2000, ACM Press, pp. 446-51.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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